

## St. JOSEPH'S COLLEGE OF ENGINEERING

(An Autonomous Institution)

St. Joseph's Group of Institutions

OMR. Chennai - 119



## SUMMARY OF CLASS WORK RECORD OF ATTENDANCE AND ASSESSMENT

Name of the staff: Dr. V. Chamundeswari

**Department of the Staff: EEE** 

Semester From: Dec 2023 To April 2024

Class & Branch: III EEE 'C'

Code No. / Subject Name: EE1672 -

**Embedded Systems (Integrated Lab)** 

Dr. VADDI SESHAGIRI RAO, M.E., M.B.A., Ph.D.
PRINCIPAL
St. JOSEPH'S COLLEGE OF ENGINEERING

(AN AUTONOMOUS INSTITUTION)
OLD MAMALLAPURAM ROAD, CHENNAI-600 119.







## St. JOSEPH'S COLLEGE OF ENGINEERING (An Autonomous Institution)

## St. JOSEPH'S INSTITUTE OF TECHNOLOGY (An Autonomous Institution)

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## SUMMARY OF CLASS WORK RECORD OF ATTENDANCE AND ASSESSMENT



Name of the Staff: Dr. V. Chamundesuca

Department of the Staff: FEE

Semester From : To 23 To

Class & Branch : TI EFF C

Code No. / Subject Name: EE 172 Enchedded System!
(Intersated Lab)



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## **EE1672 EMBEDDED SYSTEMS (INTEGRATED LAB)**

(Common to EEE and EIE)

LTPC 3024

## Objectives:

- Building blocks of Embedded System.
- Introduction to Embedded processors.
- Bus communication in processors, Input/output interfacing.
- Basics of real time operating system.
- Real-time applications of an embedded system.

## **UNIT - I INTRODUCTION TO EMBEDDED SYSTEMS**

CO1

Introduction to Embedded Systems -Building blocks of Embedded System, Structural units in Embedded processor, selection of processor & memory devices- DMA - Memory management methods- Timer and Counting devices, Watchdog Timer, Oscillator and Reset Circuits-Real Time Clock Introduction to a brief study on a typical embedded processor.

## **UNIT - II INTRODUCTION TO EMBEDDED PROCESSORS**

CO2

Introduction to PIC 16F877A microcontroller: architecture and pin diagram, Overview of instruction sets and addressing modes. Introduction to ARM processor: Architecture and pin diagram of CORTEX processor. Micro-c and Keil compilers for programming using embedded C coding.

## **UNIT - III EMBEDDED NETWORKING**

CO3

Embedded Networking: Introduction, I/O Device Ports & Buses-Serial Bus communication protocols-RS232 standard - RS422 - RS 485- Inter Integrated Circuits (I2C), Serial Peripheral Interface (SPI), CAN Bus, - USB- Wi-Fi- Bluetooth- Zigbee - need for Device Drivers.

## **UNIT - IV RTOS BASED EMBEDDED SYSTEM DESIGN**

CO4

Introduction to basic concepts of RTOS- Task, process & threads, interrupt routines in RTOS, Multiprocessing and Multitasking, Pre-emptive and non-pre-emptive scheduling, Task communication shared memory, message passing-, Inter process Communication - synchronization between processes-semaphores, Mailbox, pipes, 133 priority inversion, priority inheritance-Polling and interrupt handling mechanismOverview and comparison of commercial RTOS:VX works- µC/OS-II. **UNIT - V EMBEDDED SYSTEM APPLICATION AND DEVELOPMENT** COS

Case Study of Washing Machine- Automotive Application- Smart card System Application-ATM machine -Digital camera.

## THEORY: 45 periods PRACTICAL:30 periods TOTAL: 75 Periods

## Text Books:

- 1. Peckol, "Embedded system Design", John Wiley & Sons, 2010.
- 2. Lyla B Das," Embedded Systems-An Integrated Approach", Pearson, 2013.
- 3. Shibu. K.V, "Introduction to Embedded Systems", Second Edition, McGraw Hill, 2017.
- 4. Embedded Systems Fundamentals with Arm Cortex M Based Microcontrollers: A Practical Approach Paperback - 1 March 2017.
- 5. PIC microcontroller and Embedded systems Using Assembly and C for PIC18, second edition, 2021.

## Reference Books:

- 1. Raj Kamal, 'Embedded Systems-Architecture, Programming, Design', Second Edition, Mc Graw Hill, 2013.
- 2. C.R.Sarma, "Embedded Systems Engineering", University Press (India) Pvt. Ltd, 2013.
- 3. Taminy Noergoard, "Embedded Systems Architecture", Second Edition, Newnes, 2012.
- 4. Han-Way Huang, "Embedded system Design Using C8051", Cengage Learning, 2009.
- 5. Rajib Mall "Real-Time systems Theory and Practice" Pearson Education, 2007.



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## St. JOSEPH'S COLLEGE OF ENGINEERING

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We Make You Shine

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(An Autonomous Institution)

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OMR, Chennai - 119

## SUMMARY OF CLASS WORK

RECORD OF ATTENDANCE AND ASSESSMENT

Name of the Staff: DY. V. Chamun de Cwax i Department of the Staff: Code No. / Subject Name: £ E 1672 Embedded Systems The performance of the students in this subject in previous years:

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## TIME TABLE

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## **LESSON PLAN**

## Unit-1 INTRODUCTION TO EMBEDDED SYSTEMS

Target Period: 9

Planned Periods: 12

ON:	.NO Topics to be Covered	Course	Hours Required	Text / Reference Book	Teaching Aid	Knowledge Level	
1	Introduction to Embedded Systems, Definition		1	11	PPT and BB	ס	
7	The build process for embedded systems, Characteristics of embedded system.	-	1	11	PPT and BB	R,U	

## **LESSON PLAN**

## **UNIT IV RTOS BASED EMBEDDED SYSTEM DESIGN**

Target Period: 9

Planned Periods: 12

S.NO	Topics to be Covered	Course Outcome	Hours Required	Text/ Reference	Teaching Aid	Knowledge
-	Introduction to basic concepts of RTOS- Basics		1	11,R1	PPT and BB	R,U&An
2	Task, process & threads- Structure of process,		2	T1,R1	PPT and BB	R,U&An
e e	Multiprocessing and Multitasking- Types of Multitasking, task scheduling Interrupt routines in an RTOS.		1	T1,R1	PPT and BB	R,U,A,An&E
4	Preemptive – SRT, RR, Priority Based Scheduling and non-preemptive scheduling-		1	T1,R1	PPT and BB	R,U,A,An&E
٠, د	Task communication, shared memory- Pipes, Memory Mapped object Message passing-	C312.4	1	T1,R1	PPT and BB	R,U,A,An&E
9	Interprocess Communication – synchronization between processes-Dead lock condition, Live		2	T1,R1	PPT and BB	R,U,A,An&E
7	Semaphores, Mailbox, Pipes		1	T1,R1	PPT and BB	R,U&An
∞ ×	Priority inversion, priority inheritance- Polling and interrupt handling mechanism		1	T1,R1	PPT and BB	R,U,A,An&E
6	Comparison of Real time Operating systems: Vx Works, µC/OS- II.	,	1	T1,R1	PPT and BB	R,U&An
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## **LESSON PLAN**

Text B	Text Book: (T)		Author	Publisher	Year
S.No	Title of the Book		- 1	0	2010
F	Embedded system Design		Peckol	John Wiley & Sons	2 2
=	cmoedada system coses:		Lyla B Das	Pearson	2013
17	Embedded Systems-An Integrated Approach		Shibu, K.V	Tata McGraw Hill	2009
<u>ت</u>	Introduction to Embedded Systems  Learner of Embedded Systems  Learner of Embedded Systems  Learner of Embedded Systems			APractical Approach paperback	2017
15	Embedded Systems of For PIC18 PIC microcontroller and Embedded systems Using Assembly and C for PIC18		Muhammad Ali Mazidi, and Bolin D. McKinlav	Muhammad Ali Mazidi, Pearson Education, second edition and Rolin D. McKinlav	n 2021
Referen	Reference Book: (R)		Raikamal	McGraw Hill	2013
R	Embedded System-Architecture, Programming, Design	, Design	C D Carma	University Press (India)Pyt Ltd	2013
82	Embedded Systems Engineering		Tammy Noergaard,	Elsevier	5006
R3	Embedded Systems Architecture		Han-Way Huang,	Cengage Learning	2009
R4	Embedded system Design Using C8051		Rajib Mall	Pearson Education	2007
RS	Real-time systems Theory and Practice				
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	7 1/	Applications using embedded processors	processors	www.embedded.com	
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DAILY RECORD OF CLASS WORK

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\* Holidays / CL and OD days to be mentioned against the corresponding dates

Signature of HOD:

# DAILY RECORD OF CLASS WORK $^{24}$ $^{24}$ $^{1}$

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Signature of HOD:

ATTENDANCE

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21EE-303	312321105123	RAM PRAVEEN . M	
21EE 228	312321105124	RAMPRASANNA . A	
21EE 1.35	312321105125	RANJITH G.S	
21EE 251	312321105126	ROSELIN SWATHI, A	
21FE.307	312321105127	RUPESH N	+++++
21EE 125	312321105128	SAM TONY, Z	++++
21EE 108	312321105129	BANJAY K	+
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21EE 234	312321105133	SANTHOSH KUMAR . 3	
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21EE.310	312321104.35	SARAVANA SRINIVAS.	
21EE 301	312321105136	SARGURUNATHAN . J. P	
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21EE 235	312321105140	SIBHIRAJ. B				
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21FE 148	312321105151	SIDHANTH. 9.K				_
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21FE 22h	312321105153	SIVA KUMAR K (6)	74.	_		_
21EE318	312321105154	SNEGAPRIYA.A	Se-	1		_
21FF 275	312321105155	SOBANA V		_		_
21EE 128	312321105156	SOOM SHUDHA. 3		_		_
21EE 250	312321105157	SOUNDHARTYA. 61	_	_		
21EE 280	312321165158	SOUNDHARYA, K	-	_	_	
21EE 320	312321125159	SRENITHY. M.R	100		_	_
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21EE 224	312321105161	SRINIVAS, R	_	-	-	_
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21EE 230	312321105165	SUSITHA.T			_	
21EE.317	312321105166	SUVIN RAJ. M				_
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21EE319	312321105175	VIGNESH B VIGNESH RAJA S	16,	-		
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21EE299	312321105188	VINDUTNI . S	- {			_
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SUMMARY OF CLASS WORK	DATE: $8/\sqrt{24}$ PERIOD: $q$ TOTAL NO. OF CLASS $q$ UNIT: $\mathcal{P}$	1) Decens (	-) Car stading		DATE: 10/ (124, PERIOD: 5 TOTAL NO. OF CLASS 6 UNIT:	- Henery Schoth	-) con stulin.		DATE: $U / U_{2-Q}$ PERIOD: $U = 0$ TOTAL NO. OF CLASS $U = 0$	1) Kermon Selection.		Sign of HOD with Date
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DATE: $\frac{1}{3}/\frac{1}{2}$ PERIOD: 9 TOTAL NO. OF CLASS ("UNIT: ]	RTC - ) heal how cloud		DATE: $22/1/24$ PERIOD: $4/9$ TOTAL NO. OF CLASS   $7$	world due the		DATE: $2q/l/2\mu$ PERIOD: $6$ TOTAL NO. OF CLASS $15$ —UNIT: $1$	Johnda To a Enhedded	Spanker
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DATE: 5/2/24 PERIOD: 4 TOTAL NO. OF CLASS 12 UNIT: 1)
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DATE: 23/3/24 PERIOD: 1 TOTAL NO. OF CLASS UNIT: (1)
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# SUMMARY OF CLASS WORK PERIOD: 43 TOTAL NO. OF CLASS

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DATE: 28 2 PERIOD: 4 TOTAL NO. OF CLASS UNIT: (1)
(2) f) betweenth, 21gher Need for Dens Brien Sewina on CAN in Authornhes
DATE: $\mathcal{A}/\mathcal{F}/\mathcal{Z}$ PERIOD: $\mathcal{I}$ , $\mathcal{I}$ TOTAL NO. OF CLASS UNIT: $(I)$ , $/$
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SUMMARY OF CLASS WORK	DATE: $ (3)/3/24 $ PERIOD: $ (5)/3/24 $ TOTAL NO. OF CLASS UNIT: $ (1)/3/24 $	Task process and theraples all fremme	o prese	DATE: $2/3/2$ PERIOD: TOTAL NO. OF CLASS   UNIT: $\sqrt{1}$	Pash schooluly hylys	and its on types	DATE: $\mathcal{F}_{\mathcal{A}}$ PERIOD: 1 TOTAL NO. OF CLASS 1 UNIT: $\mathcal{V}$	problem as Icheoluling tester.	Steered Alemeny, monest fraising	Sign of HOD with Date
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JAIL 19724 PERIOD: TOTAL NO. OF CLASS UNIT: 10  The frazen Cenn - P. P.C.  (00% Brythen 1) oths  graphom s- Horbed exilami  graphom s- Horbed exilami  graphom s- Horbed exilami	DATE: 13/3 Ly PERIOD: Y TOTAL NO. OF CLASS UNIT: 10  Primily livers in one Taber-lane Servinal on (Cy diffrent frether) This and diffrent enter	DATE: $(4/3)$ PERIOD: TOTAL NO. OF CLASS / UNIT: $)$	E pollugared Johnny Radlusi heckberran Capasai of RTOS
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## **RESULT ANALYSIS**

## **MODEL EXAMINATION - I**

: 15/2/24 Date of Exam

: 66 Total No. of Students 2

: 66 No of Students Attended 3

: 53 No of Students Passed

: 80.13 7. Percentage of Pass 5

## **RESULT ANALYSIS**

Range of Marks	0 - 44	45 - 60	61-75	76- 90	91 - 100
No. of Students	13	38,	15	р	O

Signature of the Staff

Name of the Staff

21. V. chamedown

Signature of the HOD of the Concerned:

Department with Date

(After distributing the answer scripts)



## **RESULT ANALYSIS**

## **MODEL EXAMINATION - II**

: Esfsky Date of Exam : 37 Total No. of Students

: 37 No of Students Attended 3

No of Students Passed

: 83.78.). Percentage of Pass 5

## **RESULT ANALYSIS**

Range of Marks	0 - 44	45 - 60	61-75	76- 90	91 - 100
No. of Students	06	26	05	0	0

Signature of the Staff

Name of the Staff

: Dr v. chamenofurran

Signature of the HOD of the Concerned:

Department with Date

(After distributing the answer scripts)

## **RESULT ANALYSIS**

## **MODEL EXAMINATION - III**

Date of Exam

Total No. of Students

No of Students Attended

: 98 45 b3 No of Students Passed

Percentage of Pass 5

## **RESULT ANALYSIS**

Range of Marks	0 - 44	45 - 60	61-75	76-90	91 - 100
No. of Students	03	21	28	11	Hyp anne

Signature of the Staff

Name of the Staff

Signature of the HOD of the Concerned:

Department with Date

(After distributing the answer scripts)



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REMARKS

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Question Paper Code: 91223

St. JOSEPH'S COLLEGE OF ENGINEERING, CHENNAI - 119 (An Autonomous Institution)

B.E./B.Tech. DEGREE END SEMESTER EXAMINATIONS, APRIL/MAY 2024

Sixth Semester

Electrical and Electronics Engineering

EE1672 - EMBEDDED SYSTEMS (LAB INTEGRATED)

(Common to Electronics and Instrumentation Engineering) (Regulations 2021)

Time: Three Hours

Maximum: 100 Marks

## **Answer ALL questions**

PART - A (10 X 2 = 20 Marks)

What is an embedded system?

What is the need of Watchdog timer?

What is 'W' register in PIC Microcontroller?

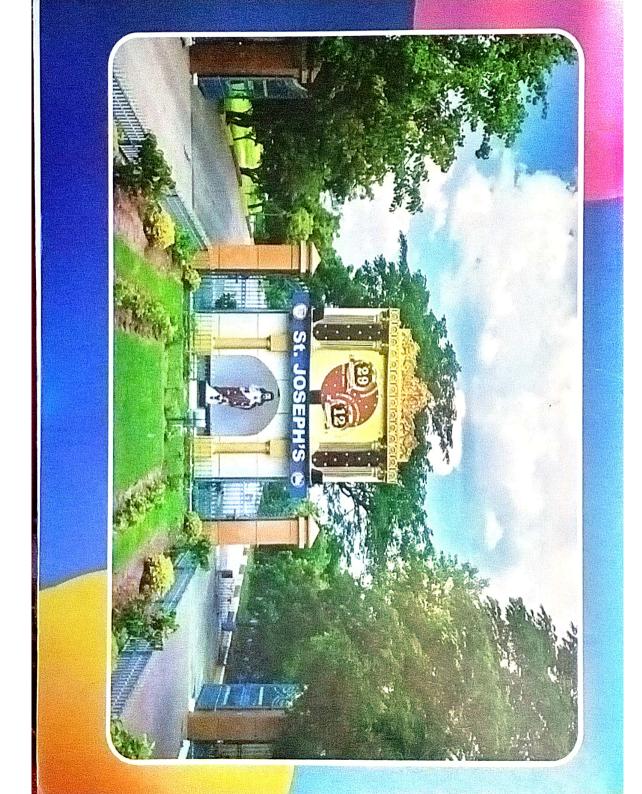
What is meant by load store architecture?

How error is detected in CAN bus?

What is 12C?

Define Task.

Define DTOC





## St. JOSEPH'S COLLEGE OF ENGINEERING

(An Autonomous Institution)

St. Joseph's Group of Institutions

OMR. Chennai - 119



## SUMMARY OF LABORATORY WORK ASSESSMENT AND ATTENDANCE RECORD

**Laboratory / Workshop : EE1278 Principles of Electrical and Electronic Devices Laboratory** 

Year: I EEE E Semester: 02

Name of the staff: Mr. T. Sriananda Ganesh, Mr. Sreekanth

**Department of the Staff: EEE** 







## You Choose, We Do It St. JOSEPH'S COLLEGE OF ENGINEERING (An Autonomous Institution)

## St. JOSEPH'S INSTITUTE OF TECHNOLOGY (An Autonomous Institution)

St. Joseph's Group of Institutions

OMR, Chennal - 119





## SUMMARY OF LABORATORY WORK

ASSESSMENT AND ATTENDANCE RECORD

Laboratory/ Workshop :	EE1278	- Princis	oles of ele	adrial Ele	clronic	1 Device
Year :		EEC	E	Semester _	02	Lako —
Name of the Staff:	Mr.T.Sr	iananda	Granosh	Ar. Sre	ekar H.	Nrs. Roma
Department :	E	LF				

	Syllabus
Ex. No.	Name of the Experiment
_	Measurement of equivalent Resistance in an electric circuit
2	Verification of ohm's law
3	Verification of Kirchhoff's laws
4	Measurement of magnetic flux in magnetic circuits.
5	Star and delta connections with balanced and unbalanced loads.
9	V-I characteristics of PN junction and Zener Diode.
7	V-I characteristics of SCR
8	V-I characteristics of BJT (CE, CB, CC Configuration).
6	V-I characteristics of FE
10	V-I characteristics of UJT and its application

## LAB COMPLETION DETAILS

	From	То	Sign of In-charge with Date	Sign of HOD with Date
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Cycle 2	23/09/2024	21/05/2024	J.S.A. hanese 21/05/24	O. P. Durk of L.
Cycle 3				

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Period / Day	Monday	Tuesday	Wednesday	Thursday	Friday

## **PLAN FOR LABORATORY EXPERIMENTS**

## EE1278 - PRINCIPLES OF ELECTRICAL AND ELECTRONIC DEVICES LABORATORY

## Course outcomes (COs): The students will be

₩ CO	COURSE OUTCOMES
CO1	Manipulate simple electric and magnetic circuits.
CO2	Become familiar with the basic ohm's and kirchhoff's law realization.
CO3	Design and Analyse the basic circuit components and connect them to make electrical circuit.
CO4	Ability to Design and construct basic load connections of electrical networks
CO5	To study and analyse the characteristics of various electronic semiconductor de
BE A DOTAL	C Demiller of the control of the con

## MAPPING BETWEEN COs, POs AND PSOs WITH CORRELATION LEVEL 1/2/3:

Course outcomes	POI	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	3	3	3	1	1	1	2	1	2	2	3	2
CO2	3	3	3	3	3	2	1	1	2	1	1	3	3	2
CO3	3	3	3	3	3	1	2	1	2	1	2	2	3	2
CO4	3	3	3	3	3	1	1	1	2	1	2	2	3	2
CO5	3	3	3	3	3	2	1	1	2	1	1	3	3	2

## RELATION BETWEEN EXPERIMENT WITH COs:

SI. No	Course Content	CO Statement
1	Measurement of equivalent Resistance in an electric circuit	CO1
2	Verification of ohm's law	CO1
3	Verification of Kirchhoff's laws	CO2
4	Measurement of magnetic flux in magnetic circuits.	CO3
5	Star and delta connections with balanced and unbalanced loads.	CO4
6	V-I characteristics of PN junction and Zener Diode.	CO5
7	V-I characteristics of SCR	CO5
8	V-I characteristics of BJT (CE, CB, CC Configuration).	CO5
9	V-I characteristics of FE	CO5
10	V-I characteristics of UJT and its application	COS

PRINCIPAL

## **ATTENDANCE**

Roll No.	Reg.No.	Month  Name Date  Batch—I Period	_	03	<u>~</u>
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216	204	KAMALT SRT.K	1	1	1
220	2072	KANIMOZHI.S	1	1	Ī
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719	5073	KANISHKA:S	1	1	1
258	50 77	LAKSHANA. K	1	1	1
129	5078	LAURA HENRIETTA J	1	1	1
25€	5082	LOSHINI-R	1	1	1
283	5083	MAHALAXMI P	1	1	1
197	5085	MANJUSHPI B	1	/	1
706	5086	Batch-Wi MANJU SREE A	1	1	1
236	5095	MYTHEE JANARTHANAN	1	A	,
118	CV98	NAMES PRINCESSTANLEY	1	1	1
30D	5700	MAVEENA CK	/	1	1
709	5706	DVIVA C	1	J	1
721	र्चान	PADALA DHONTIKA	1	1	1
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272	2110	PAVITHEA P	1	1	1
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hop Monitoring Sheet Staff (1) Mr. R. Svenkark

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## ATTENDANCE

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224	5112	PRA HALYA - P		1	1	1
245	5115	PRIYANKA . G		1	1	1
351	206	PRIVANKA · Y		/	1	1
234	5117	RATES WARIE		/	/	1
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195	5063	THMANUVEL ISAK.B		1	1	1
148	5064	TREAN HUSSAIN . Z.M		1	1	/
783	50bb	JAYA PRAKASH ·K		1	l	1
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35L	5068	JEEVAN KUMAR · D		1	/	
200	5074	KESAVAN · B		A	P	$\int$
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218	- 5075	KISHDRE · R		L	1	1
257	5076	KOWSHICK . S		1	1	1
δЫ	5079	LOGARAJA ·M		1	1	1
	5080	LOKESH · P		1	1	*/
237	5081	LOKESH KUMAR · M		1	1	1
143	5081	MAHESHWARAN · N		1	1	1
and the state of	Bar of the	BATCH-YII				
619	-5097	M. NAT. MARONAM		1	1	/
146	5099	MEGACHANDRAN .L	-	1	1	1
268	5089	MEIYARASU IK		1	1	1
29L	5090	MOHAMED RAFEE . T.J		1	1	
840	5091	MOHAMED SHUAIB, N.M		1	/	1
			Total Absent			
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skshop Monitoring Sheet
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Laboratory / Worskshop Monitoring Sheet Staff (1): Staff (2): Staff (3): Class & Branch : Lab Name & Code : Month MARKS Date 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 Attend Record Model Expt. No. 5 5 5 5 5 5 5

NAA - Not allowed to Attend

NA - Not allowed

NS - Not submitted

## **SUMMARY OF LABORATORY WORK**

**SUMMARY OF LABORATORY WORK** 

CYCLE-1 BATCH-

SROUP	EXPERIMENT NO.														
-	1 Completed Date	2 Completed Date	3 Completed Date	4 Completed Date	5 Completed Date	6 Completed Date	7 Completed Date	8 Completed Date							
A1 8	27/02/24	05/03/24 27/02/24 16/02/04/24 02/04/24	12/03/24	27/03/24	05/44/57	16/04/24									
A2	16/04/24	27/02/24	०५०%	12/03/24	27/03/2	02/4/24									
А3	02/04/29	27/02/24 16/02/04/24 12/03/24 12/03/24 13/03/24	27/02/2	०५/०३/१५	12/03/24	27/03/24	-								
A4 .	27/03/24	02/04/24	16/04/24	23/02/24	<sub>कर्</sub> श्रथ	12/03/24	1								
A5 \	263/24	22/03/24	62/64/24	16/04/24	22/02/24	०४ विश्वीभ									
A6	25/03/24	2/03/24	27/24	02/34/24	16/04/24	27/02/24	T-1	1 							
A7	A02/24	271-2124 121-2124 121-2124	12/03/24	22/03/24	00/04/24	16/04/24									
1	6/04/24	23/02/24	ospa)य	12/03/24	22/03/24	02/04/24	7'-	. ,							
A9	<sub>धरीव्य</sub> ग्रिम	16/04/24 02/03/34	27/02/24	05/03/24	12/03/24	27/03/24		= 1							
A10	7/03/24	02/04/24 16/04/24	16/at/321	22/02/24 12/03/24 05/03/24 22/02/24	०५ ००७/१५	22/03/24 12/03/24	T No.	BC 7							

O. J. A. Lane SC. . Signature of Staff Signature of HOD

CYCLE - 2 BATCH -

UP	EXPERIMENT NO.													
GROUP	1 Completed Date	2 Completed Date	3 Completed Date	4 Completed Date	5 Completed Date	6 Completed Date	7 Completed Date	8 Completed Date						
A1	23/04/24 23/04/24 21/05/24	30/4/24	07/05/24	14/05/24	2/05/24 1/4/05/24		A 5							
A2	2/05/24	गान्त्र शिक्षाय अ	कीक्गीव्य	07/05/24	對哥叫		74 m - 1							
А3	ph/2/24	27/02/24	23/4/24	30/04/24	· Hosty									
A4	076564	PAR PA	21/08/24	23/04/24	30/041	=								
A5	30/04/24	otlospy	14/05/24	2/05/24	22/4/24		-							
A6	22/04/24	30/04/24	2/05/24	1402/24	21/05/24									
A7	21/05/24	23/04/24	20/04/24	02/05/24	11/05/24		-							
A8	10/05/24	01/08/24	1204/2	2/04/24	205/24		*							
<b>A</b> 9	13/02/24	14/05/24	21/08/24	23/04/24	20/04/24	à	27 4	Ã						
A10	30/04/24	07/05/24	14/05/24	21/08/24	22/84/24	8		, , , ,						

O. S. A. hanepel Signature of Staff Signature of HOD

