Profile

Name	:	Dr.S Meena
Date of Birth	:	27.06.1981

Unique id : 1-453828401



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Degree	Specialization	Year of	University	Class
		Passing		Obtained
Ph.D	Process Control with embedded systems	Feb 2020	Sathyabama University	-
M.E	Electronics & Control	2011	Sathyabama University	I with Distinction
B.E	Electronics & Instrumentation engg	2002	Mannonmaniam Sundaranar University	I

Work Experience (From present work)

Name of the Institute /	Docignatio	Date of	Date of	No. of		
Industry	Designatio n	Joining	Relieving / Promotion	Years	Months	Days
St.Joseph's College of Engg.	Associate Professor	01.07.2013	Till date	9	9	21
St.Joseph's College of Engg.	Assistant Professor	04.07.2011	30.06.2013	1	11	27
St.Joseph's College of Engg.	Lecturer	02.06.2008	04.07.2011	3	1	3
ThangaveluEngg.Colle ge	Lecturer	01.10.2004	30.11.2005	1	1	
MuthayammalEngg College	Lecturer	06.03.2003	30.09.2004	1	6	

Course taught at Diploma / Post Diploma / Under Graduate / Post Graduate / Post Graduate Diploma (Last three years)

- Environmental System & Engg.
- Control Systems
- Microprocessor & Microcontroller
- Transducer Engg
- Principles of Management
- Principles of Electrical, Electronics and Communication engineering
- Circuit Theory
- Electronic Devices & Circuits
- Electrical Measurements
- Linear Integrated Circuits
- Digital Logic Circuits
- Digital Image Processing

S.No.	Project Title	Name of the agency	Duratio Amount
Sponsored Proj	jects		
Ph.D	:-		
Master	:1		
Research Guida	ance (counts)		

Patent Publis	hed						
S.No.		Title		Year of Registration		Year of Publica tion	Status
1.	DATA FO	EL METHOD FOR PREPARING OR PROCESSING BY ARTIFICIAL GENCE AND MACHINE NG SYSTEMS			04/12/2022		Published
2.	BASED V STENOGI COMMU	_	YPTO ETHOD FOR DATA I USING BLOCK		12/11/ 2021	Published	
Technology Transfer		Name of the Technology		Collaboration with			n
Research Publications (counts)		National Conference		ternational Conference		onal rnal	International Journal
No. of Books Published with details		-	5		2		13
Other Achiev	ements	NPTE Asso	ived TNSCST aw EL Translator for ciated With AIC itized for AICTE me.	the sul	oject mult kh schem	irate DSP e	